

H9014

General Purpose Transistors NPN Silicon

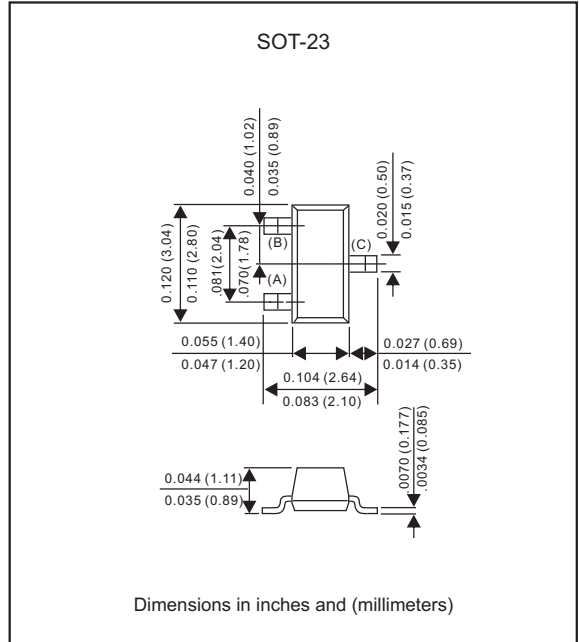
Features

- Collector current. (100mA)
- Pb-Free package is available
- Suffix "-H" indicates Halogen-free part, ex. F9014Q-H.

Mechanical data

- Epoxy:UL94-V0 rated flame retardant
- Case : Molded plastic, SOT-23
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Mounting Position : Any
- Weight : Approximated 0.008 gram

Package outline



Maximum ratings (AT T_A=25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Collector-emitter voltage	V _{CEO}	45	V
Collector-base voltage	V _{CBO}	50	V
Emitter-base voltage	V _{EBO}	5	V
Collector current -continuous	I _C	100	mA

Thermal characteristics

PARAMETER	Symbol	MIN.	TYP.	MAX.	UNIT
Total device dissipation FR-5 board (1)	T _A = 25°C	P _D			225
					Derate above 25°C
Thermal resistance	Junction to ambient	R _{θJA}		556	°C/W
Total device dissipation alumina substrate(2)	T _A = 25°C	P _D			300
					Derate above 25°C
Thermal resistance	Junction to ambient	R _{θJA}		417	°C/W
Operating junction temperature range	T _J	-55		+150	°C
Storage temperature range	T _{STG}	-55		+150	°C

1.FR-5 = 1.0 X 0.75 X 0.062 in.

2.Alumina = 0.4 X 0.3 X 0.024 in. 99.5% alumina.

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Electrical characteristics (At $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
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Off characteristics

Collector-emitter breakdown voltage	$I_C=1\text{mA}$	$V_{(BR)CEO}$	45			V
Emitter-base breakdown voltage	$I_E=100\mu\text{A}$	$V_{(BR)EBO}$	5			V
Collector-base breakdown voltage	$I_C=100\mu\text{A}$	$V_{(BR)CBO}$	50			V
Collector cutoff current	$V_{CB}=40\text{V}$	I_{CBO}			100	nA
Emitter cutoff current	$V_{EB}=3\text{V}$	I_{EBO}			100	nA

On characteristics

Collector-emitter saturation voltage	$I_C=100\text{mA}$ $I_B=5\text{mA}$	$V_{CE(sat)}$			0.3	V
DC current gain	$I_C=1\text{mA}$ $V_{CE}=5\text{V}$	h_{FE}	150		600	-

h_{FE} values are classified as follows:

*	Q	R	S
h_{FE}	150~300	200~400	300~600

Rating and characteristic curves (H9014)

FIG.1 - STATIC CHARACTERISTIC

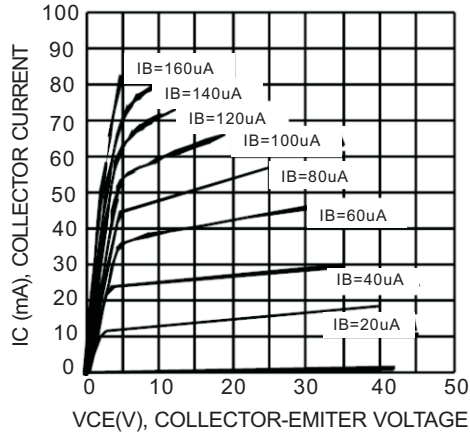


FIG.2 - DC CURRENT GAIN

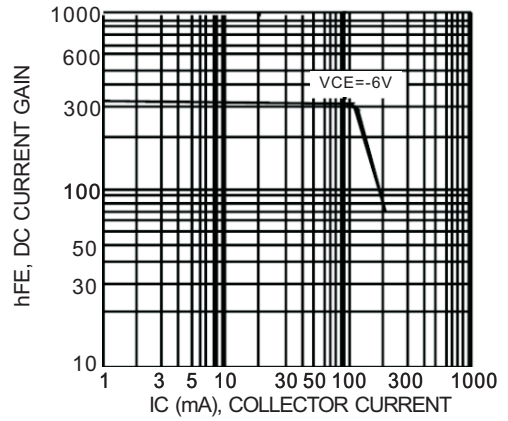


FIG.3 - BASE-EMITTER SATURATION VOLTAGE
COLLECTOR-EMITTER SATURATION VOLTAGE

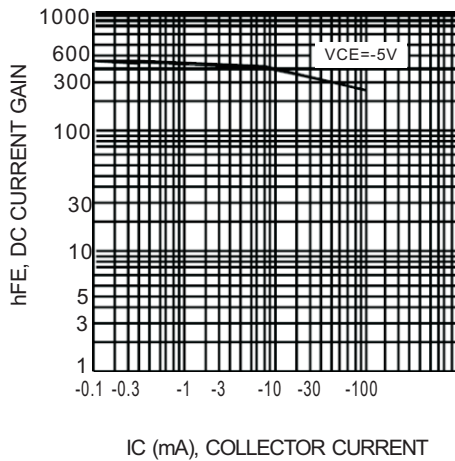
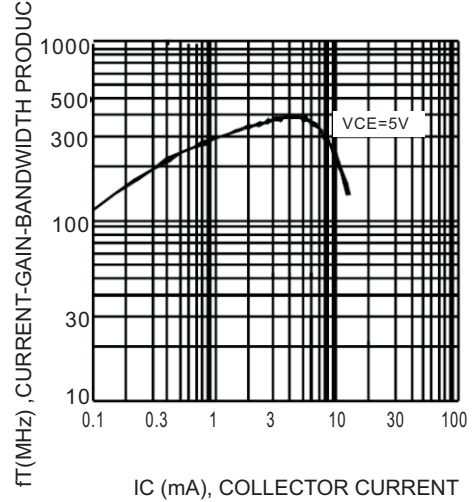
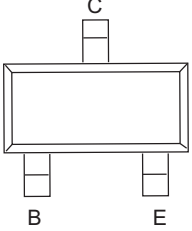
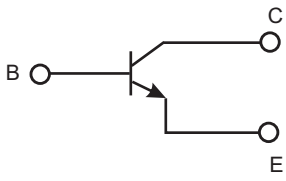


FIG.4 - CURRENT GAIN-BANDWIDTH PRODUCT



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Pinning information

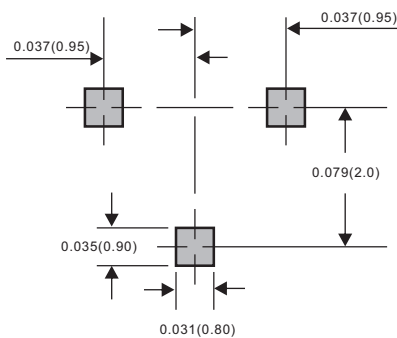
Pin	Simplified outline	Symbol
PinB Base PinC Collector PinE Emitter		

Marking

Type number	Marking code
H9014	J6

Suggested solder pad layout

SOT-23



Dimensions in inches and (millimeters)